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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Glenn A. Rinne

Application No.: 10/689,976

Group Art Unit: 2811

Filed: October 21, 2003

Examiner: not yet assigned

For: STACKED ELECTRONIC STRUCTURES INCLUDING OFFSET SUBSTRATES

Date: June 23, 2004

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

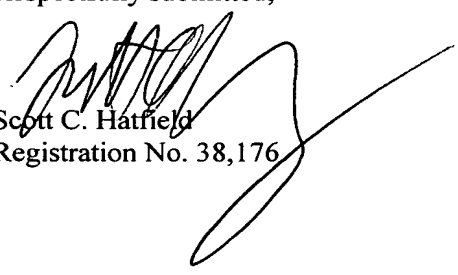
**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
FOR INTERNATIONAL SEARCH REPORT**

Sir:

Attached is a Supplemental Form PTO-1449 listing documents cited in the International Search Report for the corresponding International Application Number PCT/US 03/33211. Each document listed on the attached PTO-1449 was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement. A copy of any listed foreign patent document and/or non-patent literature including the Search Report, is enclosed. A copy of any listed U.S. patent and/or U.S. patent application publication is not provided herewith in accordance with the waiver by the U.S. Patent and Trademark Office of requirements under 37 C.F.R. § 1.98(a)(2)(i) for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003.

No fee is believed due; however, the Commissioner is hereby authorized to charge any deficiency or credit any refund to Deposit Account No. 50-0220.

Respectfully submitted,


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Joyce Paoli



